



SWORN TRANSLATION

I, Junichi ASO, hereby declare and state that I am knowledgeable of each of the Japanese and English languages and that I made the attached translation of the attached application from the Japanese language into the English language and that I believe my attached translation to be accurate, true and correct to the best of my knowledge and ability.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issued therefrom.

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Declarant : Junichi Aso
Junichi ASO



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[Title of the Invention] Method for fabricating a thin film magnetic head and wafer structure
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[Inventor]
 [Address] c/o TDK Corporation
 1-13-1, Nihonbashi, Chuo-ku, Tokyo, Japan
 [Name] Ikuhito ONODERA
[Applicant]
 [Identification Number] 000003067
 [Name] TDK Corporation
[Representative]
 [Identification Number] 100072051
 [Patent Attorney]
 [Name] Kosaku SUGIMURA
[Representative]
 [Identification Number] 100059258
 [Patent Attorney]
 [Name] Akihide SUGIMURA
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[Identification of Document] Specification

[Title of the Invention] Method for fabricating a thin film magnetic head and wafer structure

[Claims]

[Claim 1] A method for fabricating a thin film magnetic head, comprising the steps of:

fabricating a thin film magnetic head assembly on a given substrate,
fabricating, on said substrate, an electrical circuit including a circuit leading wire so as to monitor the processing degree of said thin film magnetic head assembly,

forming a protective layer so as to cover said thin film magnetic head assembly and said electrical circuit,

fabricating, on said substrate, a bump so as to penetrate said protective layer and thus, to be exposed,

fabricating, on said substrate, an element leading wire a circuit leading wire to be electrically connected to said thin film magnetic head assembly, and

electrically connecting said element leading wire and said circuit leading wire,

whereby said bump is shared with said thin film magnetic head assembly and said electrical circuit, and the processing degree of said thin film magnetic head assembly is indirectly monitored by an external monitoring system on the monitoring information from said electrical circuit to said external monitoring system via said bump.

[Claim 2] A fabricating method as defined in claim 1, further comprising the step of forming a conductive film to electrically connect said element leading wire and said circuit leading wire.

[Claim 3] A fabricating method as defined in claim 2, wherein said conductive film is made by a sputtering method or a plating method.

[Claim 4] A fabricating method as defined in claim 2, wherein said element leading wire, said circuit leading wire and said conductive film are made of the same conductive material.

[Claim 5] A fabricating method as defined in claim 1, further comprising the step of fabricating a bonding pad on said protective layer so as to be electrically connected with said bump.

[Claim 6] A fabricating method as defined in claim 5, wherein said bonding pad is elongated, on said protective layer, to the area of said electrical circuit.

[Claim 7] A fabricating method as defined in claim 6, wherein said bonding pad is narrowed in the area between said thin film magnetic head assembly and said electrical circuit.

[Claim 8] A fabricating method as defined in claim 1, wherein said electrical circuit is constructed of an electrical lap-guiding element.

[Claim 9] A fabricating method as defined in claim 8, wherein said thin film magnetic head assembly includes a reading head element, and the polishing degree of said reading head element is monitored by said electrical lap-guiding element.

[Claim 10] A wafer structure comprising:

- a thin film magnetic head assembly which is fabricated on a given substrate,
- an electrical circuit including a circuit leading wire so as to monitor the processing degree of said thin film magnetic head assembly, which is fabricated on said substrate,

- a protective layer so as to cover said thin film magnetic head assembly and said electrical circuit,

- a bump so as to penetrate said protective layer and thus, to be exposed, which is fabricated on said substrate, and

- an element leading wire to be electrically connected to said thin film magnetic head element to be connected to an external circuit, which is fabricated on said substrate

- said element leading wire being electrically connected with said circuit leading wire,

- said bump is shared with said thin film magnetic head assembly and said electrical circuit.

[Claim 11] A wafer structure as defined in claim 10, further comprising a conductive film to electrically connect said element leading wire and said circuit leading wire.

[Claim 12] A wafer structure as defined in claim 11, wherein said conductive film is made by a sputtering method or a plating method.

[Claim 13] A wafer structure as defined in claim 12, wherein said element leading wire, said circuit leading wire and said conductive film are made of the

same conductive material.

[Claim 14] A wafer structure as defined in claim 10, further comprising a bonding pad on said protective layer so as to be electrically connected with said bump.

[Claim 15] A wafer structure as defined in claim 14, wherein said bonding pad is elongated, on said protective layer, to the area of said electrical circuit.

[Claim 16] A wafer structure as defined in claim 15, wherein said bonding pad is narrowed in the area between said thin film magnetic head assembly and said electrical circuit.

[Claim 17] A wafer structure as defined in claim 10, wherein said electrical circuit is constructed of an electrical lap-guiding element.

[Claim 18] A wafer structure as defined in claim 17, wherein said thin film magnetic head assembly includes a reading head element, and the polishing degree of said reading head element is monitored by said electrical lap-guiding element.

[Detail Explanation of the present invention]

[0001]

[Industrially Applicable Field]

This invention relates to a method for fabricating a thin film magnetic head and a wafer structure.

[0002]

[Prior Art]

A complete thin film magnetic head is obtained by polishing a given surface of a thin film magnetic head assembly which is fabricated on a given substrate to form an air bearing surface (ABS). In a thin film magnetic head assembly including a magnetoresistive effective type reading element (often called as an "MR element", hereinafter), the polishing process is carried out so that the MR height is constant (the MR height is defined). Herein, the MR height is a processed length of the assembly from the end surface of the thin film magnetic head assembly.

[0003]

In this case, an electrical lap-guiding element (often called as a "lap-guiding element", hereinafter) is fabricated on the substrate where the thin film magnetic head assembly is fabricated, and thus, the polishing process is carried

out with monitoring the size of the MR height through the electrical changes of the lap-guiding element.

[0004]

Fig. 1 is a plan view showing a wafer structure in which thin film head assemblies and lap-guiding elements are fabricated on a single wafer as a substrate, and Fig. 2 is a cross sectional view of a portion of the wafer structure of Fig. 1, taken on line X-X. In Fig. 1, for simplicity, the thin film head assemblies and the lap-guiding elements are fabricated on the trailing surface 1A of the wafer 1 in a line by turns. In Figs. 1 and 2, for clarifying the characteristics of the present invention, the detail structure of the wafer structure is different from a real one.

[0005]

A thin film magnetic head assembly 10 includes element pads 2a; 2b and 3a; 3b which are exposed on the trailing surface 1A of the wafer 1. The element pads 2a and 2b are electrically connected to an MR element of the thin film magnetic head assembly 10 via leading wires 4a and 4b. The element pads 3a and 3b are electrically connected to a writing element of the thin film magnetic head assembly 10 via leading wires 5a and 5b.

[0006]

A lap-guiding element 20 includes circuit pads 21a and 21b, and is connected to a resistive film 24 made of the same material as that of the MR film to construct the MR element via leading wires 22a and 22b. The polishing process is carried out from the end surface 1B of the wafer 1. The polishing degree of the MR element is checked by the change in resistance of the resistive film 24, and thus, the MR height is monitored indirectly.

[0007]

Practically, a protective layer 15 is formed on the trailing surface 1A so as to cover the thin film head assemblies and lap-guiding elements.

[0008]

Therefore, as shown in Fig. 2, the circuit bump 27 is provided so as to penetrate the protective layer 15, and electrically connected to the leading wire 22b and the circuit pad 21b. Then, the change in resistance of the resistive film 24 is monitored through the circuit pad 21b, and the processing degree of the

ABS, that is, the MR height is indirectly controlled.

[0009]

Also, an element bump 7 is provided so as to penetrate the protective layer 15, and electrically connected to the leading wire 4a and the element pad 2a. The element bump 7 is electrically connected to the MR element via the leading wire 4a.

[0010]

[Problem to be Solved by the Invention]

With the recent development of high recording density and small cutting space, the distance between a thin film magnetic head assembly and an electrical circuit is narrowed on a substrate on which the thin film head assembly and the electrical circuit are disposed. In Fig. 2, therefore, the distance between the circuit bump 27 and the element bump 7 is also narrowed, for example.

[0011]

In this case, however, the short-circuit between the thin film magnetic head assembly 10 and the lap-guiding element 20 may occur. In addition, some cracks and chirping may be created between the circuit bump 27 and the element bump 7, and thus, some defects may be created in the resulting thin film magnetic head.

[0012]

It is an object of the present invention to provide a method for fabricating a thin film magnetic head and a wafer structure to satisfy the requirements of high recording density and small cutting space.

[0013]

[Means for Solving the Problem]

For achieving the above objects, this invention relates to a method for fabricating a thin film magnetic head, comprising the steps of:

fabricating a thin film magnetic head assembly on a given substrate,

fabricating, on said substrate, an electrical circuit including a circuit leading wire so as to monitor the processing degree of said thin film magnetic head assembly,

forming a protective layer so as to cover said thin film magnetic head assembly and said electrical circuit,

fabricating, on said substrate, a bump so as to penetrate said protective layer and thus, to be exposed,

fabricating, on said substrate, an element leading wire a circuit leading wire to be electrically connected to said thin film magnetic head assembly, and

electrically connecting said element leading wire and said circuit leading wire,

whereby said bump is shared with said thin film magnetic head assembly and said electrical circuit, and the processing degree of said thin film magnetic head assembly is indirectly monitored by an external monitoring system on the monitoring information from said electrical circuit to said external monitoring system via said bump.

[0014]

In the fabricating method of the present invention, a circuit bump may not be fabricated in an electrical circuit area. Therefore, even though thin film magnetic head assemblies and electrical circuits for monitoring are fabricated in high density on a single wafer to satisfy the requirement of high recording density, the short-circuit between each thin film magnetic head assembly and each electrical circuit which are fabricated adjacently can be inhibited because the corresponding circuit bump and the corresponding element bump are not contacted.

[0015]

Similarly, even though the space between each circuit bump and each element bump is narrowed, the creation of some cracks and chirping can be inhibited between the corresponding thin film magnetic head assembly and the corresponding electrical circuit. Therefore, the creation of defect can be inhibited in the thin film magnetic head assembly, and thus, the yielding ratio in manufacture of the resulting thin film magnetic head can be enhanced.

[0016]

In the fabricating method of the present invention, it is desired that the element pad and the circuit pad are electrically connected by a conductive film which is fabricated so as to cover the pads. In this case, the element pad and the circuit pad are electrically connected surely. The conductive film is preferably made by a sputtering method or a plating method. In order to reduce

the fabricating steps, the element pad, the circuit pad and the conductive film are preferably made of the same conductive material.

[0017]

It is desired to fabricate, on the protective layer, a bonding pad to be electrically connected to the bump. In this case, the contact area of a monitoring instrument for the processing degree can be enlarged. The bonding pad is preferably elongated to the area of the electrical circuit on the protective layer. In this case, since the terminal of the monitoring instrument can be disposed on the elongated portion of the bonding pad, the damage of the electrode portion of the bonding pad near the bump can be inhibited. Also, the bonding pad is preferably narrowed in between the thin film magnetic head assembly and the electrical circuit. In this case, when the electrical circuit area is cut off, to complete a given thin film magnetic head, short-circuit can be inhibited in the thin film magnetic head.

[0018]

In the fabricating method of the present invention, it is desired that the electrical circuit is made as an electrical lap-guiding element. Also, it is desired that the thin film magnetic head assembly includes the reading head element, and the polishing degree of the reading head element is monitored by the electrical lap-guiding element. In this case, the polishing degree of the reading element can be monitored by an external monitoring system through the electrical lap-guiding element.

[0019]

This invention also relates to a wafer structure comprising:

a thin film magnetic head assembly which is fabricated on a given substrate,
an electrical circuit including a circuit leading wire so as to monitor the processing degree of said thin film magnetic head assembly, which is fabricated on said substrate,

a protective layer so as to cover said thin film magnetic head assembly and said electrical circuit,

a bump so as to penetrate said protective layer and thus, to be exposed, which is fabricated on said substrate, and

an element leading wire to be electrically connected to said thin film

magnetic head element to be connected to an external circuit, which is fabricated on said substrate

said element leading wire being electrically connected with said circuit leading wire,

said bump is shared with said thin film magnetic head assembly and said electrical circuit.

[0020]

In the wafer structure of the present invention, a circuit bump may not be fabricated in an electrical circuit area. Therefore, even though thin film magnetic head assemblies and electrical circuits for monitoring are fabricated in high density on a single wafer to satisfy the requirement of high recording density, the short-circuit between each thin film magnetic head assembly and each electrical circuit which are fabricated adjacently can be inhibited because the corresponding circuit bump and the corresponding element bump are not contacted.

[0021]

Similarly, even though the space between each circuit bump and each element bump is narrowed, the creation of some cracks and chirping can be inhibited between the corresponding thin film magnetic head assembly and the corresponding electrical circuit. Therefore, the creation of defect can be inhibited in the thin film magnetic head assembly, and thus, the yielding ratio in manufacture of the resulting thin film magnetic head can be enhanced.

[0022]

In the wafer structure of the present invention, it is desired that the element pad and the circuit pad are electrically connected by a conductive film which is fabricated so as to cover the pads. In this case, the element pad and the circuit pad are electrically connected surely. The conductive film is preferably made by a sputtering method or a plating method. In order to reduce the fabricating steps, the element pad, the circuit pad and the conductive film are preferably made of the same conductive material.

[0023]

It is desired to fabricate, on the protective layer, a bonding pad to be electrically connected to the bump. In this case, the contact area of a monitoring instrument for the processing degree can be enlarged. The bonding pad is

preferably elongated to the area of the electrical circuit on the protective layer. In this case, since the terminal of the monitoring instrument can be disposed on the elongated portion of the bonding pad, the damage of the electrode portion of the bonding pad near the bump can be inhibited.

[0024]

Also, the bonding pad is preferably narrowed in between the thin film magnetic head assembly and the electrical circuit. In this case, when the electrical circuit area is cut off, to complete a given thin film magnetic head, short-circuit can be inhibited in the thin film magnetic head.

[0025]

In the wafer structure of the present invention, it is desired that the electrical circuit is made as an electrical lap-guiding element. Also, it is desired that the thin film magnetic head assembly includes the reading head element, and the polishing degree of the reading head element is monitored by the electrical lap-guiding element. In this case, the polishing degree of the reading element can be monitored by an external monitoring system through the electrical lap-guiding element.

[0026]

[Mode for Carrying Out the Invention]

This invention will be described in detail, with reference to figures, hereinafter. Fig. 3 is a cross sectional view showing a portion of a wafer structure according to the present invention. Similar to Figs. 1 and 2, Fig. 3 shows the state where a thin film magnetic head assembly 10 and an electrical lap-guiding element (often called as a "lap-guiding element") 20 as an electrical circuit are provided. The same reference numerals are given to the similar portions to the ones in Figs. 1 and 2. For clarifying the characteristics of the present invention, the wafer structure depicted is different from a real one.

[0027]

In the wafer structure shown in Fig. 3 according to the present invention, a leading wire 22b for the lap-guiding element 20 and a leading wire 4a for the thin film magnetic head assembly 10 are electrically connected by a conductive film 16. Then, an element bump 7 is fabricated so as to penetrate a protective film 15 and be contacted with the conductive film 16 above the leading

wire 4a. Therefore, the element bump 7 is electrically connected with the leading wires 4a and 22b.

[0028]

In this case, the leading wires 4a and 22b are electrically connected with the conductive film 16 certainly. Although the leading wires 4a and 22b may be electrically connected with another wire, the contact condition is not good. The leading wire 4a functions as an element leading wire for the thin film magnetic head, and the leading wire 22b functions as a circuit leading wire for the lap-guiding element.

[0029]

The conductive film is preferably made by a sputtering method or a plating method. In this case, the dimension of the conductive film can be freely defined.

[0030]

Moreover, the conductive film 16 is made of the same material as that of the leading wires 4a and 22b. In this case, since the ohmic contact between the conductive film 16 and the leading wires 4a; 22b can be developed, a given monitoring information can be obtained precisely from the element bump 7 via the leading wire 22b and the conductive film 16.

[0031]

In addition, if the leading wire 4a is connected with an MR element of the complete thin film magnetic head, as shown in Fig. 1, a given electrical current can be supplied to the MR element via the leading wire 4a without current loss.

[0032]

The conductive film 16 may be made of a conductive material such as Rh, Al, Ta, Au, Ag, Cu, Fe, Ni or an alloy composed of two or over of these metals.

[0033]

In Fig. 3, a bonding pad 18 made of Au or the like is fabricated on the protective layer 15 so as to be electrically connected to the element bump 7. Since the area of the bonding pad is set larger than the top surface area of the element bump 7, a given external circuit can be contacted precisely via the

bonding pad 18 when a processing degree is monitored by the lap-guiding element 20 or a given information is read out from an MR element of the complete thin film magnetic head.

[0034]

As shown in Fig. 3, the bonding pad 18 is preferably elongated to the leading wire 22b, that is, the electrical circuit area on the protective layer 15.

[0035]

The portion of the bonding pad 18 near the element bump 7 functions as an electrode terminal to read out information from an MR element of the complete thin film magnetic head, as mentioned above.

[0036]

If a given terminal is electrically connected to the portion of the bonding pad 18 near the element bump 7 in order to monitor the information of the processing degree by the lap-guiding element, the portion of the bonding pad 18 may be damaged. That is, the electrode terminal of the thin film magnetic head is damaged, and thus, the complete thin film magnetic head may be also damaged structurally.

[0037]

On the other hand, as shown in Fig. 3, if the bonding pad 18 is elongated to the electrical circuit area on the protective film 15, a given terminal can be connected to the elongated portion of the bonding pad 18. Therefore, the information of the processing degree can be monitored without the damage of the portion of the bonding pad 18 near the element bump 7 which corresponds to the electrode terminal of the complete thin film magnetic head. Since the portion of a wafer 1 on which the electric circuit area is fabricated is cut off along the line Y-Y and disposed, the complete thin film magnetic head is not damaged even though the elongated portion of the bonding pad 18 is damaged.

[0038]

Fig. 4 is a plan view of the portion of the wafer structure of Fig. 3. In Fig. 4, the bonding pad 18 includes the narrowed portions 18A and 18C between the leading wires 4a and 22b, that is, between the thin film magnetic assembly and the electrical circuit.

[0039]

As mentioned above, the thin film magnetic assembly and the electrical circuit is cut off along the line Y-Y, and thus, divided. The bonding pad 18B in the right side of the line Y-Y functions as an electrode terminal to read out information of a given MR element.

[0040]

Since the elongated portion 18C of the bonding pad 18 is a dispensable portion in the complete thin film magnetic head, it is desired that the elongated portion 18c is narrowed so as not to generate short-circuit, as mentioned above.

[0041]

Fig. 5 is a plan view showing a wafer structure at polishing using lap-guiding elements. In this case, the thin film magnetic head assembly 10 is polished in order to form the ABS with monitoring the polishing degree, that is, the MR height, by using the lap-guiding element 20.

[0042]

The leading wire 22b of the lap-guiding element 20 and the leading wire 4a of one thin film magnetic head assembly 10 placed in the right side are electrically connected with one conductive film 16. Then, the leading wire 22a of the lap-guiding element 20 and the leading wire 5b of another thin film magnetic head assembly 10 placed in the left side are electrically connected with another conductive film 16. Then, the bonding pad 18 is elongated between the thin film magnetic head assembly 10 and the lap-guiding element 20.

[0043]

The thin film magnetic assembly 10 is polished so as to form the ABS of the complete thin film magnetic head as follows. First of all, terminals of a monitoring instrument are connected to the elongated portions 18D of the bonding pad 18 in the lap-guiding element 20 so as not to damage the electrode portion of the bonding pad 18 near the thin film magnetic head assembly 10, and a given electrical current is flown between the terminals. The electrical current is flown through the resistive film 24 of the lap-guiding element 20 via the leading wires 22a and 22b.

[0044]

When the resistive film 24 is polished and thus, the resistance is changed, the current value between the elongated portions 18D of the bonding pads 18 is also changed. Therefore, the polishing degree, that is, of the end surface 1 of the wafer 1 can be monitored indirectly by measuring the change of the current value. As a result, the MR height of the complete thin film magnetic head can be defined precisely.

[0045]

Although the present invention was described in detail with reference to the above examples, this invention is not limited to the above disclosure and every kind of variation and modification may be made without departing from the scope of the present invention. For example, another electrical circuit such as an electrostatic destruction—resistance circuit or an electrostatic withstand voltage-measuring circuit may be fabricated as the electrical circuit, instead of the lap-guiding element.

[0046]

Moreover, a plurality of lap-guiding elements are fabricated on the trailing surface 1B so that the forefronts of the elements are shifted in turn by a given distance, and thus, the changes in resistance and the electrical disconnections of the lap-guiding elements may be monitored. In this case, the polishing degree of the end surface 1B of the wafer 1 can be precisely polished and thus, the ABS of the complete thin film magnetic head can be precisely defined.

[0047]

[Effect of the Invention]

As mentioned above, according to the present invention, a circuit bump may not be fabricated in an electrical circuit area. Therefore, even though thin film magnetic head assemblies and electrical circuits for monitoring are fabricated in high density on a single wafer to satisfy the requirement of high recording density, the short-circuit between each thin film magnetic head assembly and each electrical circuit which are fabricated adjacently can be inhibited because the corresponding circuit bump and the corresponding element bump are not contacted. Similarly, even though the space between each circuit bump and each element bump is narrowed, the creation of some cracks and

chirping can be inhibited between the corresponding thin film magnetic head assembly and the corresponding electrical circuit. Therefore, the creation of defect can be inhibited in the thin film magnetic head assembly, and thus, the yielding ratio in manufacture of the resulting thin film magnetic head can be enhanced.

[Brief Description of the Drawings]

[Fig. 1] It is a plan view showing a conventional wafer structure.

[Fig. 2] It is a cross sectional view of a portion of the wafer structure of Fig. 1, taken on line X-X.

[Fig. 3] It is a cross sectional view showing a portion of a wafer structure according to the present invention.

[Fig. 4] It is a plan view of the portion of the wafer structure of Fig. 3.

[Fig. 5] It is a plan view showing a wafer structure at polishing using lap-guiding elements.

[Description of the Reference Numerals]

1: wafer (substrate)

1A: trailing surface of wafer

2a, 2b, 3a, 3b: element pad

4a, 4b, 5a, 5b: leading wire

6: coil

7: element bump

8: element bonding pad

~~10: thin-film-magnetic head assembly~~

15: protective layer

16: conductive film

18: bonding pad

20: electric circuit (lap-guiding element)

21a, 21b: circuit pad

22a, 22b: leading wires

27: circuit bump

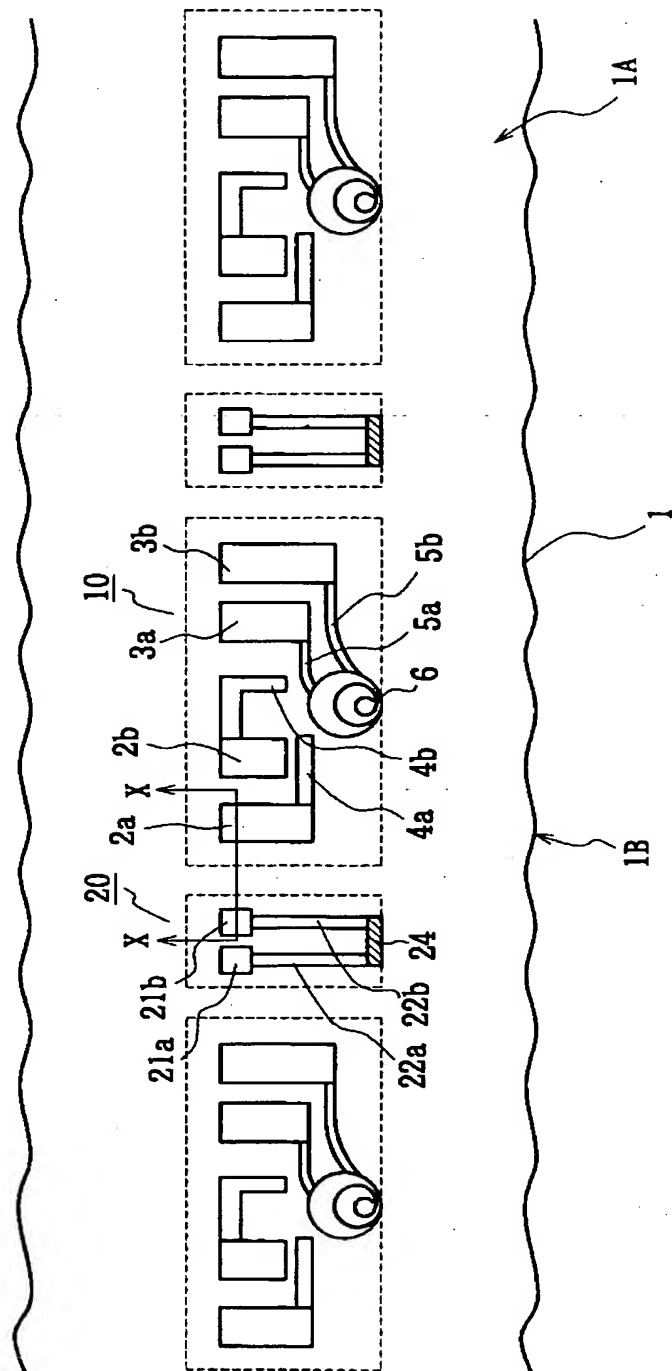
28: circuit bonding pad

[Identification of Document] Drawing

【書類名】 図面

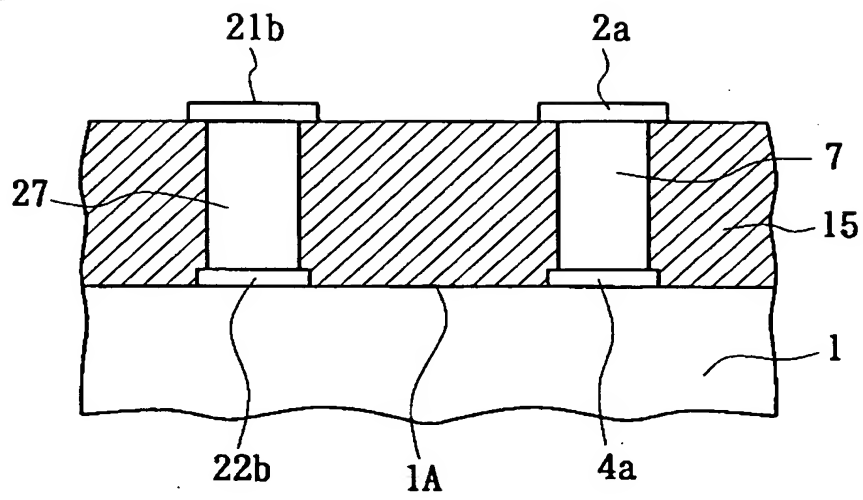
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[Fig. 1]



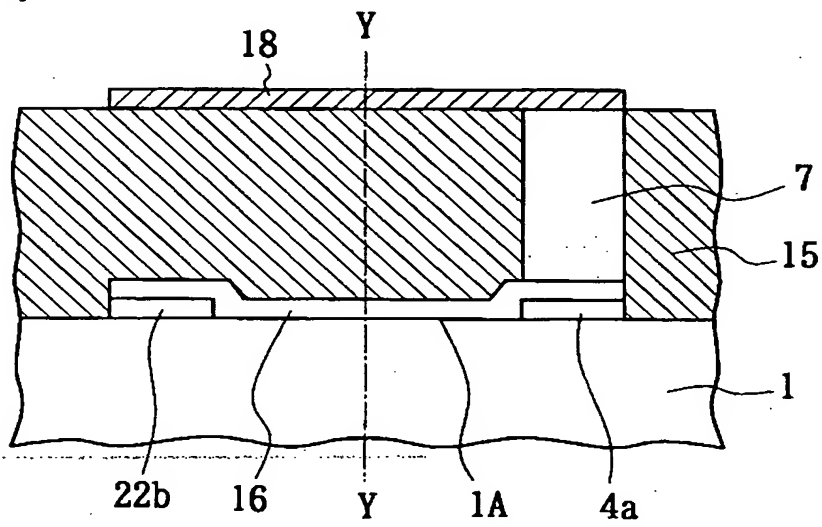
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[Fig. 2]

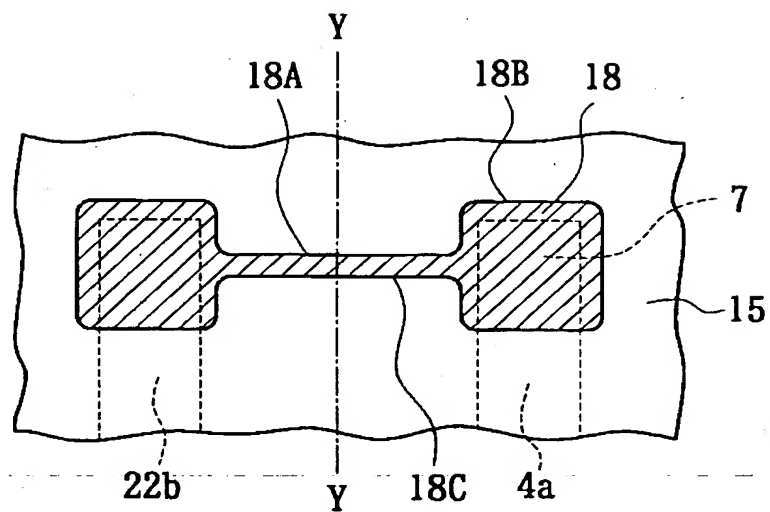


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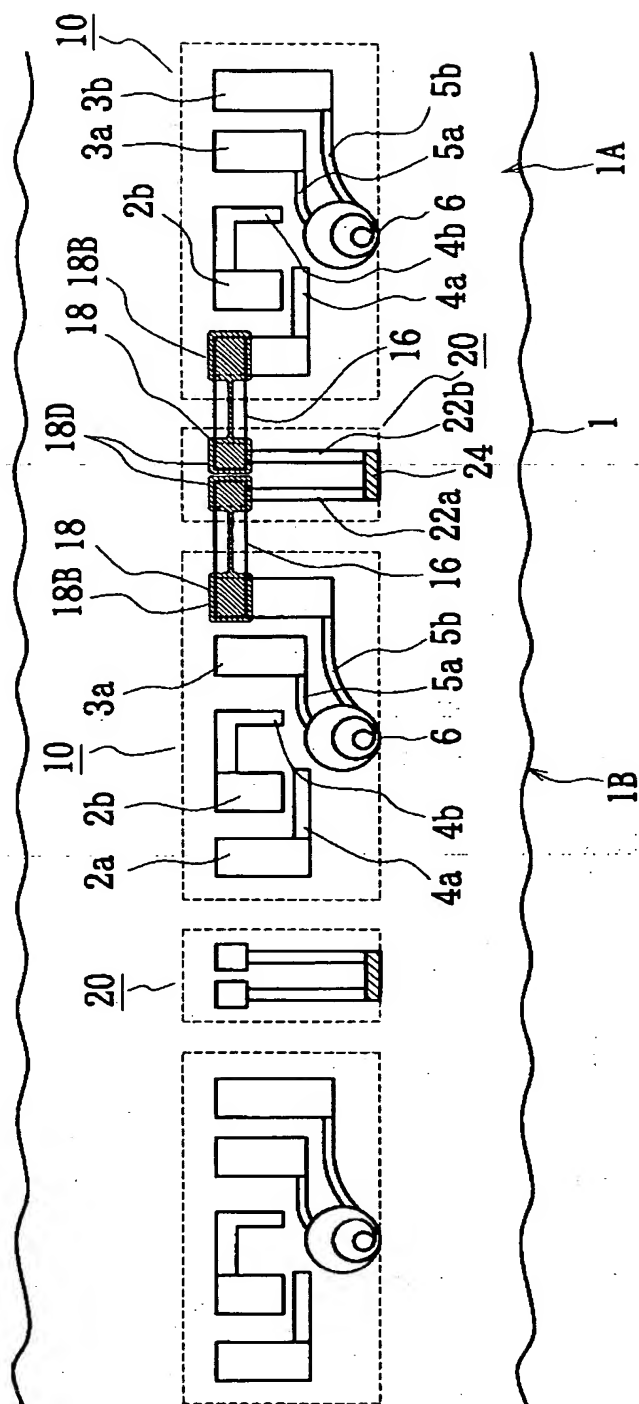
[Fig. 3]



【図4】
[Fig. 4]



【図5】
[Fig. 5]





[Identification of Document] Abstract

[Abstract]

[Object] A given magnetic film is formed on an insulating film precisely.

[Solving Means] A leading wire 22b of an electrical circuit 20 and a leading wire 4a of a thin film magnetic head assembly 10 are electrically connected with a conductive film 16. An element bump 7 is shared with the electrical circuit 20 and the thin film magnetic assembly 10. Information of the processing degree of the thin film magnetic head assembly 10 is electrically monitored via the element bump 7 from a bonding pad 18 which is electrically connected with the element bump 7.

[Selected Figure] Fig. 3